

REFERENCES – PLD

Dueck, Robert K., *Digital Design with CPLD Applications and VHDL*, 2nd edition, Thomson/Delmar Learning, 2005.

This textbook is useful as an introduction to complex programmable logic devices and the software tools used in programming. It also includes a CD-ROM with simulation files from the examples presented in the book.

Floyd, Thomas L., *Digital Fundamentals with VHDL*, Prentice Hall, 2003.

This book introduces digital logic concepts and functions with an emphasis on using VHDL for designing PLDs. It emphasizes applications, troubleshooting, and real-world situations. It also includes EWB and Multisim circuit files on to provide troubleshooting practice.

Maxwell, Clive, *BeBop to the Boolean Boogie*, High Text Publications, 1995.

This book covers fundamental electronics concepts as well as components and processes. It is a technical book that is easy to read and understand.

Maxwell, Clive, *Designus Maximus Unleashed!*, Butterworth Heinemann, 1998.

This is a collection of 30 articles that were originally published in periodicals. Articles include a subjects from logic synthesis to nanotechnology and includes field programmable devices.

Maxwell, Clive, *Design Warrior's Guide to FPGAs*, Newnes/Elsevier, 2004.

This book is written in an easy-to-read style and covers FPGA technology and applications.

Zeidman, Bob, *Designing with FPGAs & CPLDs*, CMP Books, 2002.

This reference book describes the procedure for designing and choosing the right programmable logic devices.